



**NATIONAL INSTITUTE OF TECHNOLOGY GOA**  
**(An Institution of National Importance under MHRD, Govt. of India)**

**Applications are invited for recruitment of JRF under SERB Project**  
“Interference rejection using built-in filter based Band-pass Sigma Delta Modulators”.

**Specializations for the JRF**

Sl. No.	Position	Name of the Project/ Scheme	Area of Specialization	Duration	Consolidated Salary	Number of Vacancy
1	JRF	SERB	M. Tech in Electronics & Comm. Engg  Knowledge Analog and Mixed Signal Design	Initially for the period of one year which may be extended	Rs. 35,000/-  (Per month)	01

**Eligibility:**

1. PhD/M. Tech./M.E./ equivalent in Microelectronics & VLSI / or E & C with strong exposure to Analog and Mixed Signal Design, CAD Tools with at least 6.5 CGPA or 60 percent marks in aggregate from a recognized technical institute or university/ university as a Full time program along with GATE qualification.

**APPLICATION CHECKLIST (AT THE TIME OF INTERVIEW)**

The completed application (duly signed) must be produced with following self-attested documents at the time of interview.

- i) Photocopy of marks sheet/Grade card of the secondary (10), higher secondary (10+2),
- ii) Photocopy of the certificate/provisional certificate of B.E/B.Tech, M.E/M. Tech, and Ph.D. examination.
- iii) GATE Qualification Certificate.
- iv) Photo copy of proof of date of birth
- v) No-objection certificate from the present employer, if applicable.



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**Attention:**

1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
4. Shortlisted Candidates will be informed by email about the interview date. So, the Candidate must provide valid E-mail IDs in their applications.
5. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
6. Applicants in employment (private, government or any other organization) are required to submit a “No Objection Certificate” from the employer at the time of interview.
7. No TA/DA will be paid for appearing in the interview.
8. Bio-data format is being provided with this notification. The candidates are required to bring duly filled in bio-data format along with photocopies of required documents.
9. Last date for receiving the duly filled in application is **8.11.2021**.
10. Intimation regarding written test/interview shall be posted on the Institute website

**How to apply for JRF under SERB.**

The application form and other relevant information for JRF under serb project may be downloaded from the Institute website: [www.nitgoa.ac.in](http://www.nitgoa.ac.in). Applications form, duly filled in, should reach, NIT Goa, 403401 on or before **8.11.2021** and soft copy of the application can be sent to [nithin.shastri@nitgoa.c.in](mailto:nithin.shastri@nitgoa.c.in).

**NOTE:** The envelope containing the application should be superscribed as “**Application for JRF under SERB Project “Interference rejection using built-in filter based Band-pass Sigma Delta Modulators”, Department of ECE**”.

**ADDRESS FOR CORRESPONDENCE**

**Registrar,  
National Institute of Technology Goa,  
Farmagudi, Ponda, 403401  
Goa- India,  
Telephone: 0832-2404220  
Email: [nithin.shastri@nitgoa.ac.in](mailto:nithin.shastri@nitgoa.ac.in)  
Website: [www.nitgoa.ac.in](http://www.nitgoa.ac.in)**



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**Application Form for SERB Project “Interference rejection using built-in filter based Band-pass Sigma Delta Modulators”**

1. Post Applied for : JRF

2. Name of the Candidate (BLOCKLETTER):

3. Father’s Name (BLOCKLETTER):

4. Mother’s Name (BLOCKLETTER):

Paste here a  
recent  
Passport size  
Photograph

5. (a) Date of Birth: (DD/MM/YYYY)\_\_\_\_\_

(b) Sex (Male/Female/Other):\_\_ \_

(c) Marital Status: Married/Single

(d) Category: SC/ST/OBC/PWD/Open

6. Previous Research experience: (use additional sheet if required)\_\_\_\_\_

7. Publication(s), if any: (use additional sheet if required)\_\_\_\_\_

8. GATE/ NET (if any): Qualified (Yes/No): Score: \_\_\_\_\_ Rank: \_\_\_\_\_  
Specialization: \_\_\_\_\_ Year: \_\_\_\_\_

9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

Name of Exam Passed	Name of the School/College/Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA



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10. (a) Address for Communication: (BLOCKLETTER)

(b) Contact No (Mob)

(c) E-mail ID

11. Contact Details of two referees:

	Referee I	Referee II
Name :		
Designation :		
Organization:		
Office Address :		
Office Phone Number:		
Email ID:		

12. Experience on EDA Tools: Cadence \_\_\_\_\_ Synopsys \_\_\_\_\_

Mentor \_\_\_\_\_ Linux \_\_\_\_\_ Others \_\_\_\_\_ (in years) (use additional

sheet if required)

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

Signature

Note- If any information is required, extra A4 sheet may be attached.